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07/25/2001

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EXAMINER

DO, CHAT C

ART UNIT

PAPER NUMBER

2193

MAIL DATE

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03/17/2009

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No.	Applicant(s)	
	09/911,764	PAPATHANASIOU, APOSTOLIS	
	Examiner	Art Unit	
	Chat C. Do	2193	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 09 January 2009.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 20-34 and 36-53 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 20-34 and 36-53 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. This communication is responsive to Amendment filed 01/09/2009.
2. Claims 20-34 and 36-53 are pending in this application. Claims 20 and 37 are independent claims. In Amendment, claims 1-19 and 35 are cancelled. This Office Action is made non-final after a RCE filed 01/09/2009.

Claim Rejections - 35 USC § 112

3. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.
4. Claims 20-34 and 36-53 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the enablement requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention.

Re claim 20, the newly added limitations "DSP that enables adaptive filtering of adaptation observations for communication and control signals, wherein during said adaptive filtering, the DSP enables:" was not sufficiently described in the specification in such a detail way as to enable one skilled in the art to make and/or use the invention. The original specification merely mentions the term DSP in page 3 but does not fully address the above limitation.

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Re claim 37, the newly added limitation "one or more processors" was not described in the specification in such a detail way as to enable one skilled in the art to make and/or use the invention. Claims 48-53 have the similar rejection.

Thus, claims 21-34, 36-38 and 39-53 are also rejected for being dependent on the rejected base claims 21 and 37 respectively.

Claim Rejections - 35 USC § 101

5. 35 U.S.C. 101 reads as follows:

Whoever invents or discovers any new and useful process, machine, manufacture, or composition of matter, or any new and useful improvement thereof, may obtain a patent therefor, subject to the conditions and requirements of this title.

6. Claims 20-34 and 36-53 are rejected under 35 U.S.C. 101 because the claimed invention is directed to non-statutory subject matter.

Claims 20-34 and 36-53 cite a system for filtering in accordance with a mathematical algorithm. However, claims 20-34 and 36-53 merely disclose series steps/components for filtering including transformation and computation but without disclosing a practical/physical application. Further, the claims 37-53, the one or more processors, can be considered as software modules. Therefore, claims 20-34 and 36-53 are directed to non-statutory subject matter.

Claim Rejections - 35 USC § 103

7. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

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(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

8. Claims 20, 23-34, 36-37 and 40-53 are rejected under 35 U.S.C. 103(a) as being unpatentable over Martin et al. ("Unitary ESPRIT: How to Obtain Increased Estimation Accuracy with a Reduced Computational Burden") in view of the admitted prior art.

Re claim 20, Martin et al. disclose in the article a system for processing signals (e.g. abstract in page 1232 and conclusion remark in page 1241 by filtering/reconstructing the original waveform/signal), the system comprising: transforming adaptation observations from a complex arithmetic to two corresponding sets of real number arithmetic observations by means of binary orthogonalization transformation (BOT) (e.g. page 1232 right column lines 3-17 which transforming/converting the complex matrices into a set of real matrices); computing two corresponding sets of real number arithmetic adaptation parameters by applying a solution algorithm to said two corresponding sets of real number arithmetic observations (e.g. section B as real implementation in page 1236 wherein least square solution can be applied here); and transforming said two sets of real adaptation parameters to a set of complex number arithmetic adaptation parameters using an inverse binary orthogonalization transform (IBOT) (e.g. as reversed processed of BOT above, page 1232 right column lines of first paragraph, and right column lines 8-10 page 1232), and utilizing at least a portion of single set of complex arithmetic adaptation parameters for the adaptive filtering of the adaptation observations (e.g. abstract in page 1232 and

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conclusion section in page 1241 for filtering/reconstructing the original waveform/signal using the above technique).

Martin et al. fail to disclose (1) a DSP that enables adaptive filtering of adaptation observations for communication and control signals and (2) explicitly spell-out the term LESS as a means for computing two sets of real number arithmetic adaptation parameters by applying two real number Least Square Solvers (LESS) to said two sets of real number arithmetic observations. However, the admitted prior art discloses in page 1 a DSP that enables adaptive filtering of adaptation observations for communication and control signals (e.g. page 1 second paragraph) and a means for computing two sets of real number arithmetic adaptation parameters by applying two real number Least Square Solvers (LESS) to said two sets of real number arithmetic observations (e.g. last two paragraphs in page 1 wherein LESS is common and most widely used in solving such systems of linear equations).

Therefore, it would have been obvious to a person having ordinary skill in the art at the time the invention is made to add a DSP that enables adaptive filtering of adaptation observations for communication and control signals and a means for computing two sets of real number arithmetic adaptation parameters by applying two real number Least Square Solvers (LESS) to said two sets of real number arithmetic observations as taught in the admitted prior art to Martin et al.'s invention because it would enable to solve unknowns in linear equations efficiently (e.g. as common and widely used in many practical application in page 1 of the original application).

Re claims 23-24, Martin fails to disclose the LESS comprises a Recursive Least Squares algorithm (RLS) and a Least Mean Squares (LMS) algorithm. However, the admitted prior art discloses the LESS comprises RLS and LMS (e.g. last two paragraphs in page 1 of original specification).

Therefore, it would have been obvious to a person having ordinary skill in the art at the time the invention is made to add the LESS comprises RLS and LMS as taught in the admitted prior art to Martin et al.'s invention because it would enable to solve unknowns in linear equations efficiently (e.g. as common and widely used in many practical application in page 1 of the original application).

Re claims 25-27, Martin et al. fail to disclose LESS is a Householder transformation; Cholesky decomposition; and QR Decomposition (QRD). However, the admitted prior art discloses the LESS is a Householder transformation (e.g. last paragraph in page 4); Cholesky decomposition (e.g. last paragraph in page 4); and QR Decomposition (QRD) (e.g. last two paragraphs in page 1).

Therefore, it would have been obvious to a person having ordinary skill in the art at the time the invention is made to add the LESS is a Householder transformation; Cholesky decomposition; and QR Decomposition (QRD) as taught in the admitted prior art to Martin et al. 's invention because it would enable to solve unknowns in linear equations efficiently (e.g. as common and widely used in many practical application in page 1 of the original application).

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Re claim 28, Martin et al. fail to disclose the RLS is computed by a systolic array. However, the admitted prior art discloses the RLS is computed by a systolic array (e.g. page paragraph in page 4).

Therefore, it would have been obvious to a person having ordinary skill in the art at the time the invention is made to add the RLS is computed by a systolic array as taught in the admitted prior art to Martin et al.'s invention because it would enable to improve the system performance (e.g. by implementing the systolic array).

Re claims 29-30, Martin et al. fail to disclose the LESS utilizes one or more a Block Matched Filter Estimator (BMFE), a Block Zero Forcing Estimator (BZFE), and/or a Block Minimum Mean Square Error Estimator (BMMSEE) and computed via one or both a Cholesky decomposition and a QR Decomposition (QRD). However, the admitted prior art discloses in Figure 1 the LESS utilizes one or more a Block Matched Filter Estimator (BMFE), a Block Zero Forcing Estimator (BZFE), and/or a Block Minimum Mean Square Error Estimator (BMMSEE) (e.g. last four lines of last paragraph in page 4) and computed via one or both a Cholesky decomposition and a QR Decomposition (QRD) (e.g. last paragraph in page 4).

Therefore, it would have been obvious to a person having ordinary skill in the art at the time the invention is made to add the LESS utilizes one or more a Block Matched Filter Estimator (BMFE), a Block Zero Forcing Estimator (BZFE), and/or a Block Minimum Mean Square Error Estimator (BMMSEE) and computed via one or both a Cholesky decomposition and a QR Decomposition (QRD) as taught in the admitted prior art to Martin et al.'s invention because it would enable to solve unknowns in linear

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equations efficiently (e.g. as common and widely used in many practical application in page 1 of the original application).

Re claim 31, it has similar limitations cited in claim 20. Thus, claim 31 is also rejected under the same rationale as cited in the rejection of rejected claim 20.

Re claims 32-34 and 36, Martin et al. do not disclose linear system performing one or more of temporal, spatial, joint temporal, and/or spatial channel estimation of the signal; spatial channel equalization, carrier frequency estimation, Direction of Arrival (DOA) estimation, and joint carrier frequency and DOA estimation, an adaptive filter, and channel estimation, system parameter estimation, channel equalization, recursive updating of output parameters, non-recursive updating of output parameters, and system identification. However, the admitted prior art discloses in Figure 1 linear system performing one or more of temporal, spatial, joint temporal, and/or spatial channel estimation of the signal; spatial channel equalization, carrier frequency estimation, Direction of Arrival (DOA) estimation, and joint carrier frequency and DOA estimation, an adaptive filter, and channel estimation, system parameter estimation, channel equalization, recursive updating of output parameters, non-recursive updating of output parameters, and system identification (e.g. page 1 and page 4 as under background of invention and admitted prior art Figure 1).

Therefore, it would have been obvious applications to a person having ordinary skill in the art at the time the invention is made to apply the linear system performing one or more of temporal, spatial, joint temporal, and/or spatial channel estimation of the signal; spatial channel equalization, carrier frequency estimation, Direction of Arrival

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(DOA) estimation, and joint carrier frequency and DOA estimation, an adaptive filter, and channel estimation, system parameter estimation, channel equalization, recursive updating of output parameters, non-recursive updating of output parameters, and system identification as taught in the admitted prior art to Martin et al.'s invention because it would enable to reduce the complexity of computing the estimated signals.

Re claim 37, it is a system claim having similar limitations cited in claim 20. Thus, claim 37 is also rejected under the same rationale as cited in the rejection of rejected claim 20.

Re claim 40, it is a system claim having similar limitations cited in claim 23. Thus, claim 40 is also rejected under the same rationale as cited in the rejection of rejected claim 23.

Re claim 41, it is a system claim having similar limitations cited in claim 24. Thus, claim 41 is also rejected under the same rationale as cited in the rejection of rejected claim 24.

Re claim 42, it is a system claim having similar limitations cited in claim 25. Thus, claim 42 is also rejected under the same rationale as cited in the rejection of rejected claim 25.

Re claim 43, it is a system claim having similar limitations cited in claim 26. Thus, claim 43 is also rejected under the same rationale as cited in the rejection of rejected claim 26.

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Re claim 44, it is a system claim having similar limitations cited in claim 27. Thus, claim 44 is also rejected under the same rationale as cited in the rejection of rejected claim 27.

Re claim 45, it is a system claim having similar limitations cited in claim 28. Thus, claim 45 is also rejected under the same rationale as cited in the rejection of rejected claim 28.

Re claim 46, it is a system claim having similar limitations cited in claim 29. Thus, claim 46 is also rejected under the same rationale as cited in the rejection of rejected claim 29.

Re claim 47, it is a system claim having similar limitations cited in claim 30. Thus, claim 47 is also rejected under the same rationale as cited in the rejection of rejected claim 30.

Re claim 48, it is a system claim having similar limitations cited in claim 31. Thus, claim 48 is also rejected under the same rationale as cited in the rejection of rejected claim 31.

Re claim 49, it is a system claim having similar limitations cited in claim 32. Thus, claim 49 is also rejected under the same rationale as cited in the rejection of rejected claim 32.

Re claim 50, it is a system claim having similar limitations cited in claim 33. Thus, claim 50 is also rejected under the same rationale as cited in the rejection of rejected claim 33.

Re claim 51, it is a system claim having similar limitations cited in claim 34. Thus, claim 51 is also rejected under the same rationale as cited in the rejection of rejected claim 34.

Re claim 52, it is a system claim having similar limitations cited in claim 35. Thus, claim 52 is also rejected under the same rationale as cited in the rejection of rejected claim 35.

Re claim 53, it is a system claim having similar limitations cited in claim 36. Thus, claim 53 is also rejected under the same rationale as cited in the rejection of rejected claim 36.

9. Claims 21-22 and 38-39 are rejected under 35 U.S.C. 103(a) as being unpatentable over Martin et al. ("Unitary ESPRIT: How to Obtain Increased Estimation Accuracy with a Reduced Computational Burden") in view of the admitted prior art, as applied to the base claims 20 and 37, and further in view of Camp et al. (U.S. 5,592,517).

Re claims 21-22, Martin et al. fail to disclose two real number LESS are applied in parallel and series. However, the admitted prior art discloses in Figure 1 the LESS are applied in either series or parallel manner (e.g. component 100 in Figure 1) and further Camp et al. disclose in Figures 1-7 a filtering method having component structures in either series or parallel manner (e.g. claims 7 and 10 respectively).

Therefore, it would have been obvious to a person having ordinary skill in the art at the time the invention is made to apply the LESS in parallel and series manner as conceptually manner computation of Camp et al. and the admitted prior art's invention

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into Martin's invention because it would enable to improve the system performance (e.g. parallel manner would increase the speed operation and series manner would increase the dependency).

Re claim 38, it is a system claim having similar limitations cited in claim 21. Thus, claim 38 is also rejected under the same rationale as cited in the rejection of rejected claim 21.

Re claim 39, it is a system claim having similar limitations cited in claim 22. Thus, claim 39 is also rejected under the same rationale as cited in the rejection of rejected claim 22.

Response to Amendment

10. The amendment filed 01/09/2009 is objected to under 35 U.S.C. 132(a) because it introduces new matter into the disclosure. 35 U.S.C. 132(a) states that no amendment shall introduce new matter into the disclosure of the invention. The added material which is not supported by the original disclosure is as follows:

The newly added limitations in the amended claims "DSP that enables adaptive filtering of adaptation observations for communication and control signals, wherein during said adaptive filtering, the DSP enables:" and "one or more processors" are considered new matter introduced into the disclosure.

Applicant is required to cancel the new matter in the reply to this Office Action.

Response to Arguments

11. Applicant's arguments filed 01/09/2009 have been fully considered but they are not persuasive.

- a. The applicant argues in page 14-15 that the support for newly added limitations are seen in several places within the specification including pages 2, 3 and 8.

The examiner respectfully submits that the examiner has reviewed these pages in the original specification and does not see the fully or sufficient support for the newly added limitations as addressed above. The examiner agrees with the applicant that certain terms of the added limitation do appear in the original specification, however the original specification does not provide sufficient support for how the DSP can enable the adaptive filtering of adaptive observations for communication and control signals and where is/are the support for more than one processors in the specification.

- b. The applicant argues in pages 15-16 for claims rejected under 35 U.S.C. 101 that the current amendment should overcome the 101 rejection including a DSP that enables adaptive filtering of ...as cited in the claimed invention.

The examiner respectfully submits that claim 37 does not have the above alleged limitation. For claim 20, it is maintained the 101 rejection in view of the 112th first paragraph rejection.

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c. The applicant argues in pages 18-24 for claim 20 and 37 that the cited references do not disclose several limitations within the claims, particularly the newly added limitation "DSP that enables adaptive filtering ...", the transformation/conversion from a complex to two corresponding set of real numbers, and the LESS algorithm is applied to both of the sets of real numbers.

The examiner respectfully submits that newly added limitations "DSP that enables adaptive filtering..." is disclosed or taught by the admitted prior art in page 1 of the present application wherein DSP is the device/component that process a digital signal/data which is fully disclosed by the admitted prior art. The primary reference by Martin discloses the main feature/limitation of the claiming invention as transforming the complex arithmetic to two set of real numbers as seen in page 1232 which recited in page 19 of the remark wherein Martin clearly states a Unitary ESPRIT effectively DOUBLES the number of data samples...of real-valued matrices of the same size.... This paragraph is interpreted as the complex data set is transformed into double real data sets wherein the data set as matrices having the same size as the complex data set or complex data matrices. With the given understanding and reasonable interpretation, Martin clearly discloses the above alleged limitation/feature which is a transformation/conversion of a complex arithmetic to two corresponding set of real numbers. Further, LESS algorithm is not new which is fully disclosed in the admitted prior art and further the admitted prior art does not explicitly exclude LESS from taking real/ multiple real data inputs. Thus, it is reasonable to

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combine the LESS teaching into transformation of Martin to reduce the computation complexity wherein real computation is always less complexity than the complex computation. In addition, the claimed invention does not need a number of LESS as argued by the applicant.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to CHAT C. DO whose telephone number is (571)272-3721. The examiner can normally be reached on Tue-Fri 9:00AM to 7:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lewis Bullock can be reached on (571) 272-3759. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Chat C. Do/
Primary Examiner, Art Unit 2193

March 16, 2009

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